

sd
C

a memory circuit for storing said digital data from the digital signal source;
a playback circuit for reproducing said digital data stored in said memory
circuit;
an inner battery; and
a battery switch,
wherein said battery switch enables to use power from said digital source
having a higher operating voltage than that of said inner battery when said memory
circuit stores said digital data in a condition of connecting to said digital source, and
to use power from said inner battery when said playback circuit reproduces said
digital data in a condition of being removed from said digital source.

sd
C

59. (Once Amended) A memory apparatus according to Claim 55, wherein
said inner battery is a rechargeable battery; and where said memory apparatus
comprises a recharge circuit to use said higher operating voltage from said digital
source to rapidly recharge said rechargeable battery during data transfer from said
digital source to said memory apparatus.

REMARKS

This Amendment After Final Rejection is responsive to the paper identified
above, and is responsive in any manner indicated below.

PERFECTION OF FOREIGN PRIORITY - ACKNOWLEDGED

Applicant thanks the Examiner for acknowledgment of the perfection of
foreign priority under 35 USC §119 in the present application.